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1 Techniques for obtaining high performance in Java programs

Iffat H. Kazi, Howard H. Chen, Berdenia Stanley, David J. Lilja

September 2000 **Computing Surveys (CSUR)**, Volume 32 Issue 3

Publisher: ACM

Full text available: Pdf (816.13 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 77, Downloads (12 Months): 487, Download

This survey describes research directions in techniques to improve the performance of Java programs. The standard technique for Java execution is interpreted. This leads to poor performance due to the lack of portability of programs. ...

Keywords: Java, Java virtual machine, bytecode-to-source translators, interpreters, just-in-time compilers

2 Pin: building customized program analysis tools with dynamic instrumentation

Chi-Keung Luk, Robert Cohn, Robert Muth, Harish Patil, Artur Klauser, Geetha Reddi, Kim Hazelwood

June 2005 **PLDI '05: Proceedings of the 2005 ACM SIGPLAN conference on programming language design and implementation**

Publisher: ACM

Full text available: Pdf (300.61 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 66, Downloads (12 Months): 569, Download

Robust and powerful software instrumentation tools are essential for program analysis, performance evaluation, and bug detection. To meet this need, we have called *Pin*. Our goals are ...

Keywords: dynamic compilation, instrumentation, program analysis tools

Also published in:

June 2005 **SIGPLAN Notices** Volume 40 Issue 6

3 LLVA: A Low-level Virtual Instruction Set Architecture

Vikram Adve, Chris Lattner, Michael Brukman, Anand Shukla, Brian Gaeke

December 2003 **MICRO 36: Proceedings of the 36th annual IEEE/ACM International Conference on Computer Design**

Publisher: IEEE Computer Society

Full text available: Pdf (196.08 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 32, Download

A virtual instruction set architecture (V-ISA) implemented via a process can provide great flexibility to processor designers. Recent examples such as existing hardware instruction ...

4 A simplified java bytecode compilation system for resource-constrained platforms

 Carmen Badea, Alexandru Nicolau, Alexander V. Veldenbaum

September 2007 **CASES '07: Proceedings of the 2007 international conference on embedded systems**

Publisher: ACM

Full text available:  Pdf (439.70 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 62, Download

Embedded platforms are resource-constrained systems in which performance requirements are often critical. However, standard techniques such as full adaptive optimization (AO) may ...

Keywords: adaptive optimization, embedded systems, java virtual machine, superoperators

5 Communications of the ACM: Volume 51 Issue 8

 August 2008 **Communications of the ACM**

Publisher: ACM

Full text available:  Digital Edition ,  Pdf (6.85 MB) Additional Information: [full citation](#)

Bibliometrics: Downloads (6 Weeks): 299, Downloads (12 Months): 2192, Download

6 A dynamic binary instrumentation engine for the ARM architecture

 Kim Hazelwood, Artur Klauser

October 2006 **CASES '06: Proceedings of the 2006 international conference on embedded systems**

Publisher: ACM

Full text available:  Pdf (455.03 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 99, Download

Dynamic binary instrumentation (DBI) is a powerful technique for analyzing and modifying running programs. Numerous DBI frameworks have been developed for general-purpose architectures, but support for embedded architectures has been ...

Keywords: binary instrumentation, dynamic translation, embedded architectures

7 Dynamic metrics for java

 Bruno Dufour, Karel Driesen, Laurie Hendren, Clark Verbrugge

November 2003 **OOPSLA '03: Proceedings of the 18th annual ACM SIGPLAN conference on objects, languages, and applications**

Publisher: ACM 

Full text available:  Pdf (222.67 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 15, Downloads (12 Months): 148, Download

In order to perform meaningful experiments in optimizing compilation a usually rely on a suite of benchmark programs of interest to the optimiz are described as *numeric*, ...

Keywords: Java, dynamic metrics, execution traces, optimization, prof

Also published in:

November 2003 **SIGPLAN Notices** Volume 38 Issue 11

8 Statistically rigorous java performance evaluation

◆ Andy Georges, Dries Buytaert, Lieven Ceckhout
October 2007 **OOPSLA '07**: Proceedings of the 22nd annual ACM SIGPLAN systems and applications

Publisher: ACM  [Request Permissions](#)

Full text available:  [Pdf](#) (1.63 MB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 23, Downloads (12 Months): 178, Downl

Java performance is far from being trivial to benchmark because it is affected by many factors: the application, its input, the virtual machine, the garbage collector, the heap, the operating system, the run-time causes ...

Keywords: benchmarking, data analysis, java, methodology, statistics

Also published in:

October 2007 **SIGPLAN Notices** Volume 42 Issue 10

9 HPC.NET - are CLI-based Virtual Machines Suitable for High Perform

Werner Vogels

November 2003 **SC '03**: Proceedings of the 2003 ACM/IEEE conference on Supercomputing

Publisher: IEEE Computer Society

Full text available:  [Pdf](#) (163.35 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 53, Downlo

The Common Language Infrastructure is a new, standardized virtual machine that runs on several platforms. In this paper we review whether this technology has potential for the scientific community, for example by ...

10 LLVM: A Compilation Framework for Lifelong Program Analysis & Tr

Chris Lattner, Vikram Adve

March 2004 **CGO '04**: Proceedings of the international symposium on Code generation and optimization: directed and runtime optimization

Publisher: IEEE Computer Society

Full text available:  [Pdf](#) (256.90 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 32, Downloads (12 Months): 190, Downl

This paper describes LLVM (Low Level Virtual Machine), a compiler framework for lifelong program analysis and transformation for arbitrary programs, by performing compiler transformations at compile-time, ...

11 Information systems outsourcing: a survey and analysis of the literature

 Jens Dibbern, Tim Goles, Rudy Hirschheim, Sandula Jayatilaka

November 2004 **SI GMIS Database**, Volume 35 Issue 4

Publisher: ACM

Full text available:  Pdf (1.51 MB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 355, Downloads (12 Months): 3208, Download Rank: 1000

In the last fifteen years, academic research on information systems (IS) outsourcing has grown rapidly. This paper provides a survey and analysis of the field of outsourcing research. We have collected 113 papers from the literature and taken a collective ...

Keywords: determinants, literature review, outcomes, outsourcing, research foundations

12 Minimizing dependencies within generic classes for faster and smaller code

 Dan Tsafrir, Robert W. Wisniewski, David F. Bacon, Bjarne Stroustrup

October 2009 **OOPSLA '09: Proceeding of the 24th ACM SIGPLAN conference on object-oriented programming, languages and applications**

Publisher: ACM 

Full text available:  Pdf (604.18 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 12, Downloads (12 Months): 12, Download Rank: 1000

Generic classes can be used to improve performance by allowing compilers to generate more efficient code. However, the scope of compile-time polymorphism is narrower than that of runtime polymorphism. In this paper we advocate a programming ...

Keywords: generalized hoisting, generics, scary assignments and initialization

Also published in:

October 2009 **SIGPLAN Notices** Volume 44 Issue 10

13 A fast, memory-efficient register allocation framework for embedded systems

 Sathyanarayanan Thammmanur, Santosh Pandit

November 2004 **Transactions on Programming Languages and Systems**

Publisher: ACM 

Full text available:  Pdf (1.01 MB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 67, Download Rank: 1000

In this work, we describe a "just-in-time," <i>usage density-based register allocation</i> framework for embedded systems with a limited general-purpose register set wherein speed, code size, and power concern. The main ...

Keywords: Code generation, compiler optimizations, compilers, dynamic register allocation

14 The ExoVM system for automatic VM and application reduction

 Ben L. Titzer, Joshua Auerbach, David F. Bacon, Jens Palsberg

June 2007 **PLDI '07: Proceedings of the 2007 ACM SIGPLAN conference on programming language design and implementation**

implementation

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (510.60 KB)

Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 64, Download

Embedded systems pose unique challenges to Java application development. One of these challenges is the memory footprint of both the virtual machine and the application code, which is rapidly increasing ...

Keywords: VM design, VM modularity, dead code elimination, embedded systems, initialization, static analysis, static compilation

Also published in:

June 2007 **SIGPLAN Notices** Volume 42 Issue 6

15 Communications of the ACM: Volume 52 Issue 10

 October 2009 Communications of the ACM

Publisher: ACM

Full text available:  Digital Edition ,  Pdf (7.38 MB) Additional Information: [full citation](#)

Bibliometrics: Downloads (6 Weeks): 6420, Downloads (12 Months): 6420, Download

16 Design of the Java HotSpot™ client compiler for Java 6

 Thomas Kotzmann, Christian Wimmer, Hanspeter Mössenböck, Thomas Rößler, May 2008 **Transactions on Architecture and Code Optimization (TACO)**

Publisher: ACM  [Request Permissions](#)

Full text available:  Pdf (1.14 MB) Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 40, Downloads (12 Months): 433, Download

Version 6 of Sun Microsystems' Java HotSpot™ VM ships with a redesigned client compiler that includes several research results of the last years. The client compiler is now the default ...

Keywords: Java, compiler, deoptimization, intermediate representation, register allocation

17 Revisiting Out-of-SSA Translation for Correctness, Code Quality and Performance

Benoit Boissinot, Alain Darte, Fabrice Rastello, Benoit Dupont de Dinechin, March 2009 **CGO '09: Proceedings of the 2009 International Symposium on Code Generation and Optimization**

Publisher: IEEE Computer Society

Full text available:  Pdf (287.20 KB) Additional Information: [full citation](#), [abs](#)

Bibliometrics: Downloads (6 Weeks): 19, Downloads (12 Months): 80, Download

Static single assignment (SSA) form is an intermediate program representation that can be performed with fast and easy-to-implement algorithms. However, so far, where the SSA variables ...

Keywords: SSA form, Compilers, JIT-compilation

18 Using continuous statistical machine learning to enable high-speed functional instruction-set simulators Daniel Christopher Powell, Björn FrankeOctober 2009 **CODES+ ISSS '09**: Proceedings of the 7th IEEE/ACM international codesign and system synthesis**Publisher:** ACM Full text available:  Pdf (807.00 KB)Additional Information: [full citation](#), [abs](#)**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 5, Download

Functional instruction set simulators perform instruction-accurate simulations. Unlike their slower, but cycle-accurate counterparts however, they are run at a higher level ...

Keywords: continuous statistical machine learning, instruction set simulation**19 Application of a multi-criteria simulation optimization based DSS for textile dyeing scheduling**

A. Azadeh, S. F. Ghaderi, A. Dabbagh, M. Dehghanbaghi

July 2007 **SCSC**: Proceedings of the 2007 summer computer simulation conference**Publisher:** Society for Computer Simulation InternationalFull text available:  Pdf (209.58 KB)Additional Information: [full citation](#), [abs](#)**Bibliometrics:** Downloads (6 Weeks): 4, Downloads (12 Months): 43, Download

In this study we propose a decision support system for a textile dyeing scheduling problem. The simulation model itself, enables managers to find a schedule of jobs on the basis of decision parameters) ...

Keywords: goal programming, metamodel, multi-criteria decision making, scheduling factors**20 Online performance auditing: using hot optimizations without getting hot** Jeremy Lau, Matthew Arnold, Michael Hind, Brad CalderJune 2006 **PLDI '06**: Proceedings of the 2006 ACM SIGPLAN conference on programming language implementation**Publisher:** ACM Full text available:  Pdf (281.62 KB)Additional Information: [full citation](#), [abs](#)**Bibliometrics:** Downloads (6 Weeks): 13, Downloads (12 Months): 103, Download

As hardware complexity increases and virtualization is added at more layers, the performance impact of optimizations becomes increasingly difficult. Performing substantial development ...

Keywords: Java, feedback-directed optimizations, virtual machines

Also published in:

June 2006 **SIGPLAN Notices** Volume 41 Issue 6

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